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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Peter Haneder et al.
Applic. No. : 09/801,209
Filed : March 7, 2001
Title : Ferroelectric Transistor, Use Thereof In A Memory Cell
Configuration And Method Of Producing The Ferroelectric
Transistor
Examiner : Thao X. Le Art Unit: 2814

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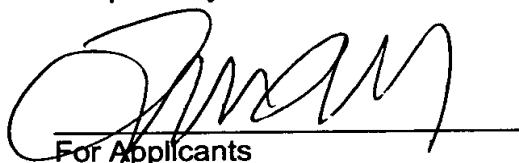
Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

Please recognize MARKUS NOLFF (Reg. No. 37,006) as my associate in the matter in the above-identified application, with full powers. Please continue addressing all communications to the following address:

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Respectfully submitted,


For Applicants

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Date: August 6, 2002

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